

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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| APPL NUM | FILING D. | CLASS | SUBCLASS | EXAMINER |
| 10028814 | 12/20/2001 | 357 438 | 774 | 2826 2842 Mandala |

**APPLICANTS: Tomsio Nayon; Liebermensch Avi;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

| | | | |
|---|---|----------------------------------|--------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> | ATTORNEY DOCKET NO |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no | | | P6459/SUN-01-02 |
| 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no | | | |
| Verified and Acknowledged Examiners's initials | | | |
| TITLE : Resistive vias for controlling impedance and terminating I/O signals at the package level | | | |

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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|--|-----------|---------------------------|------------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| Assistant Examiner | | | |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. Print Fig. |
| | | Application Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | |
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